

Fig. 1

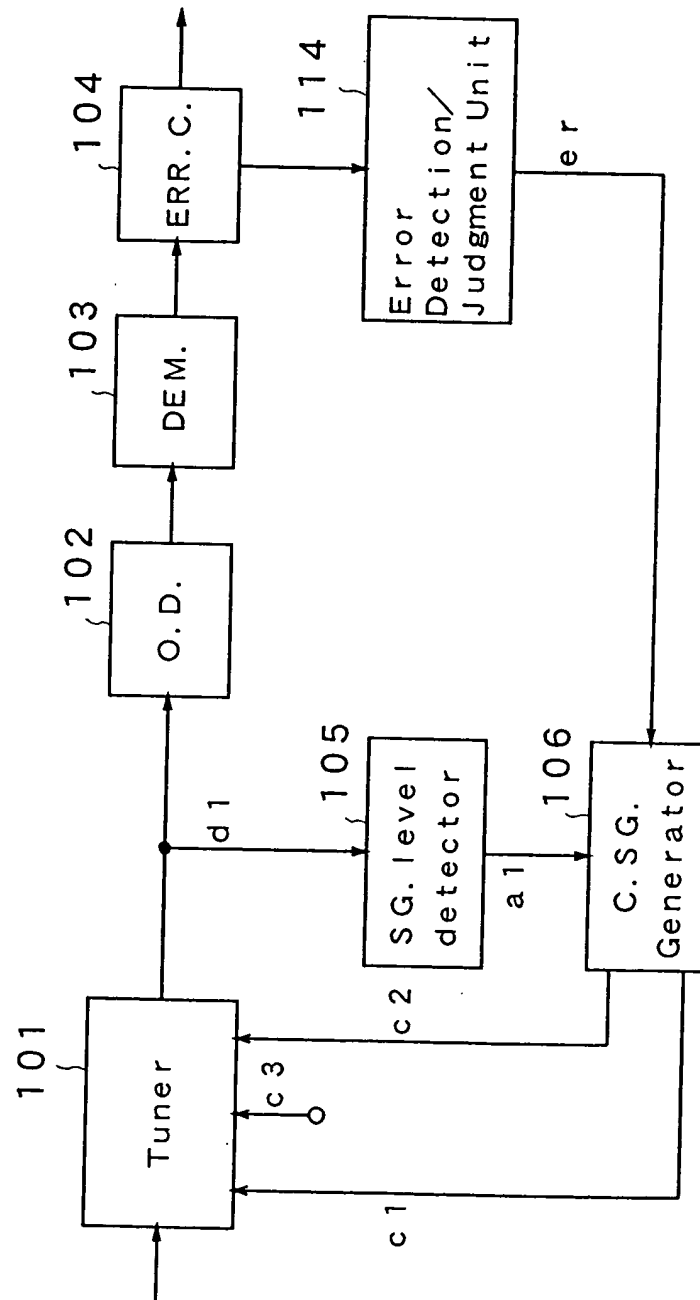


Fig. 2

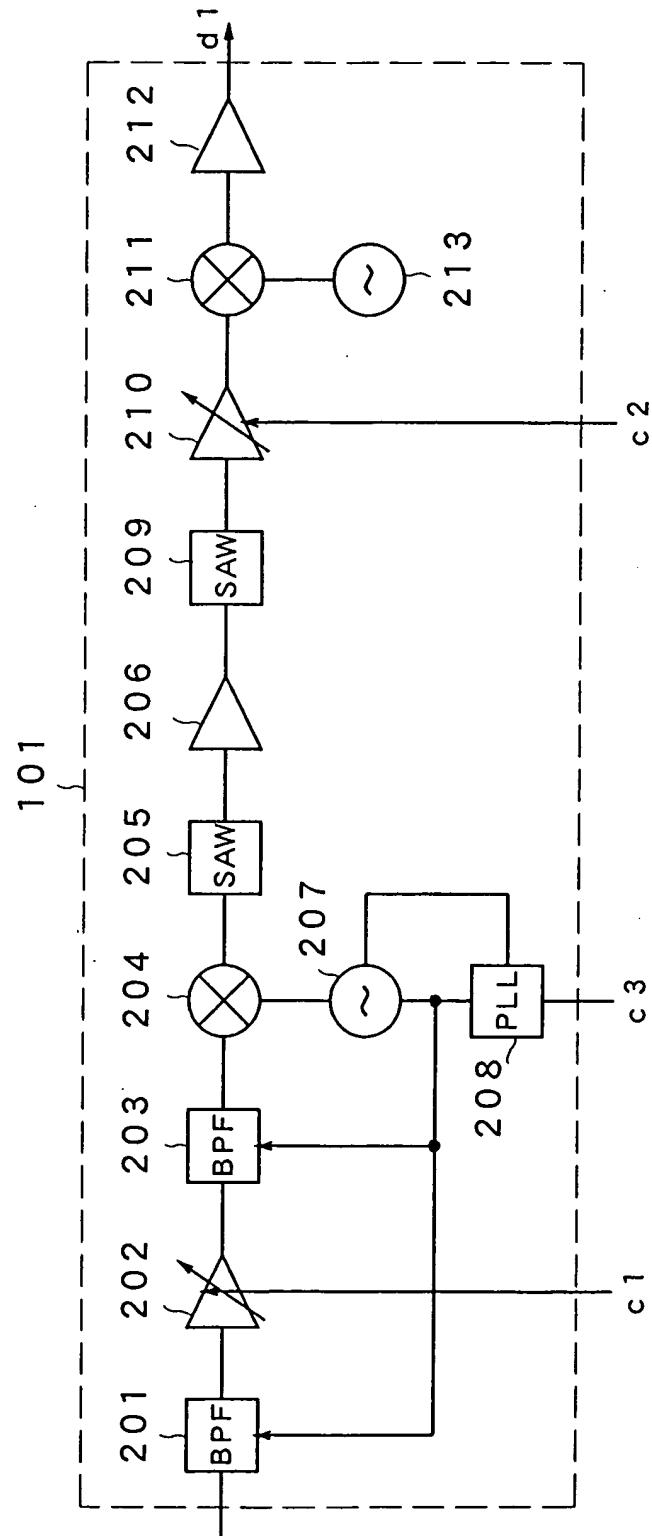


Fig. 3

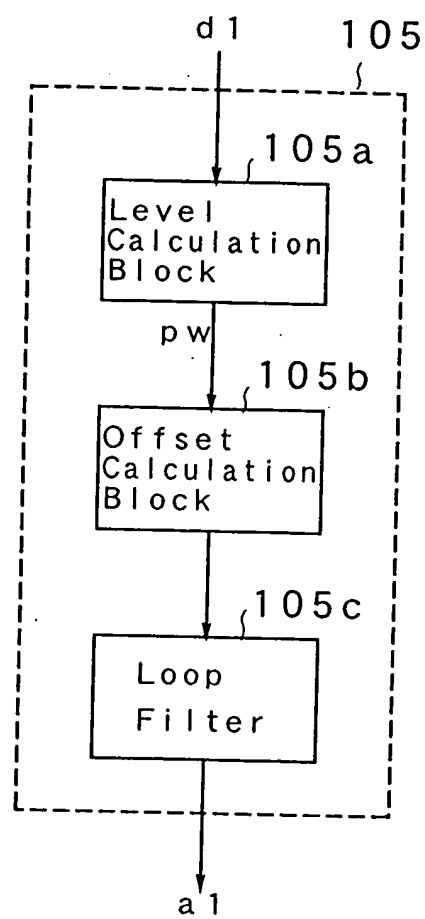


Fig. 4A

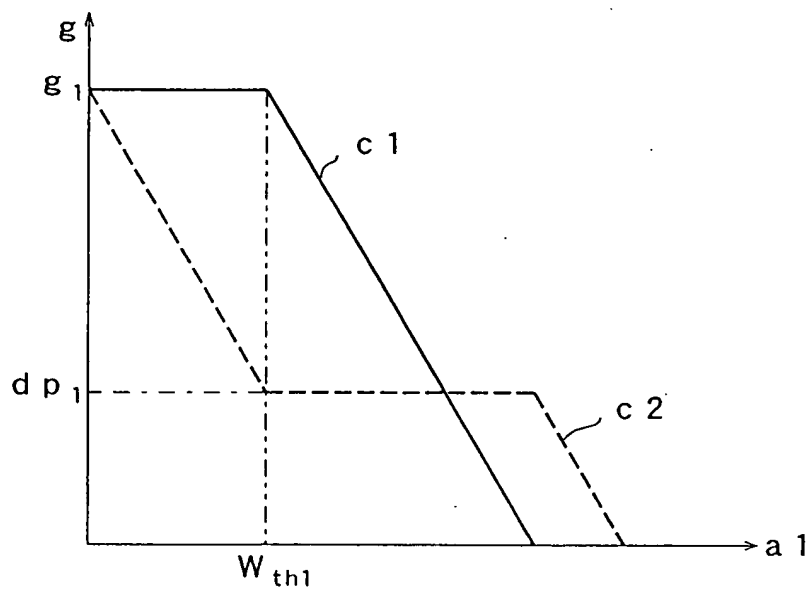


Fig. 4B

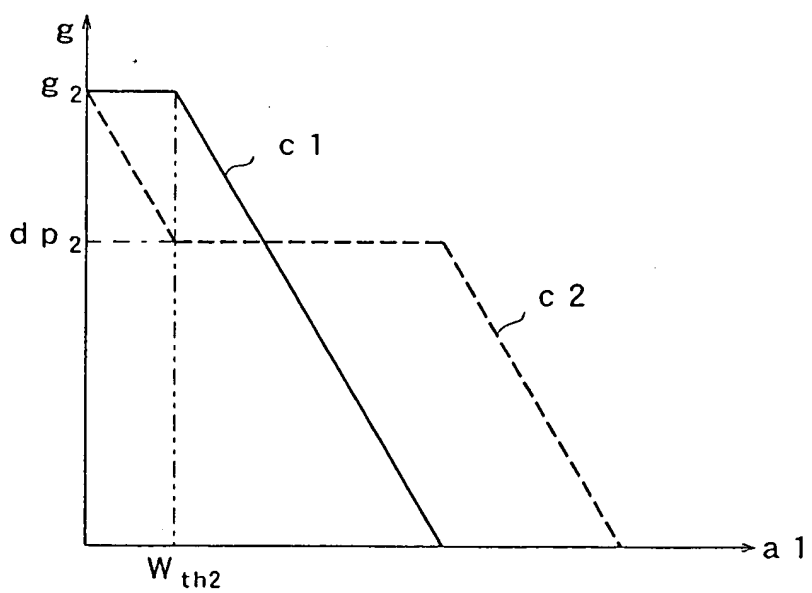


Fig. 5

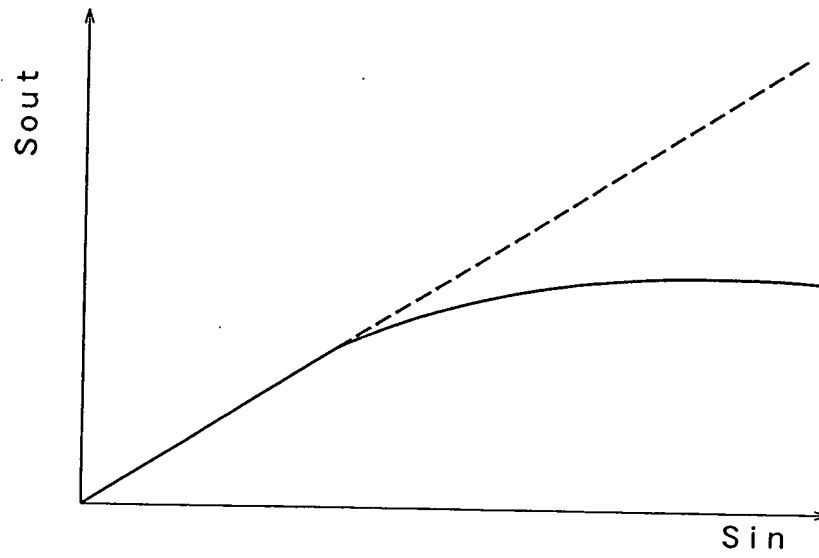


Fig. 6

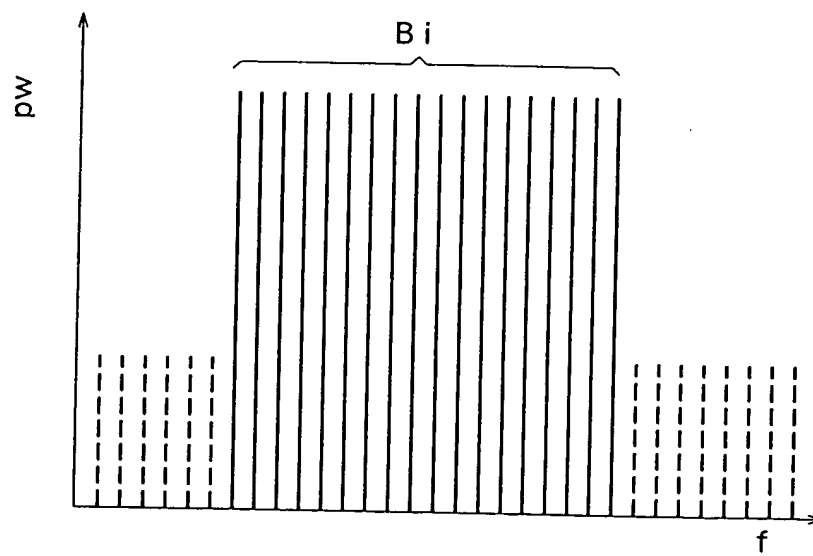


Fig. 7

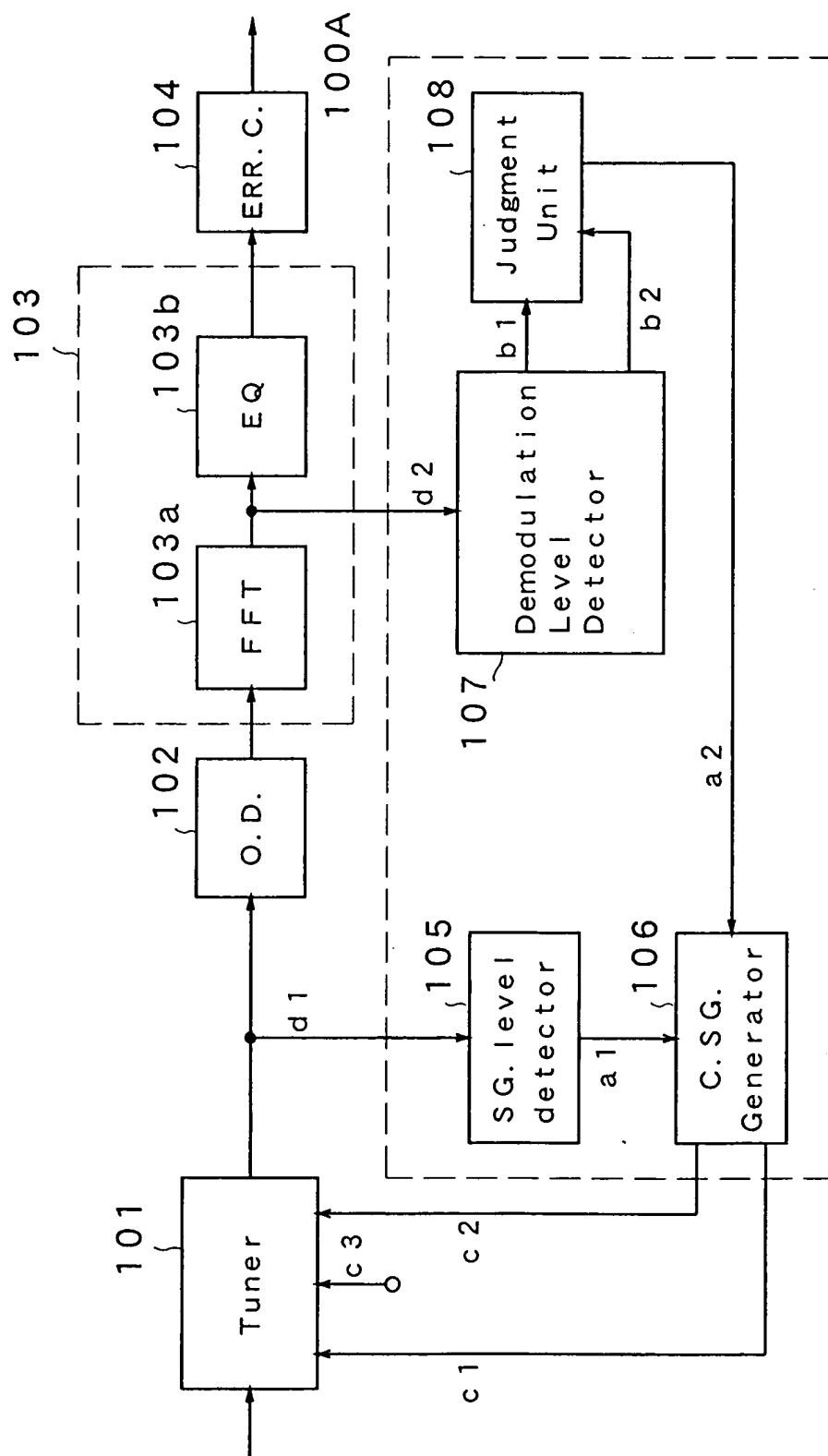
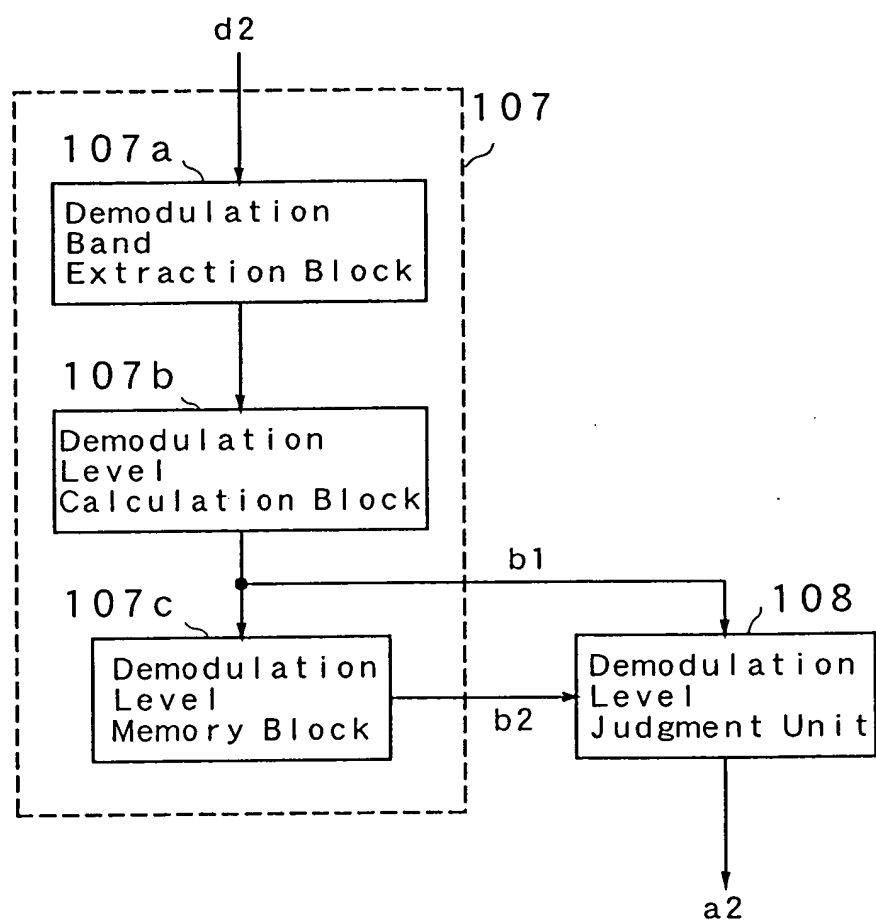


Fig. 8



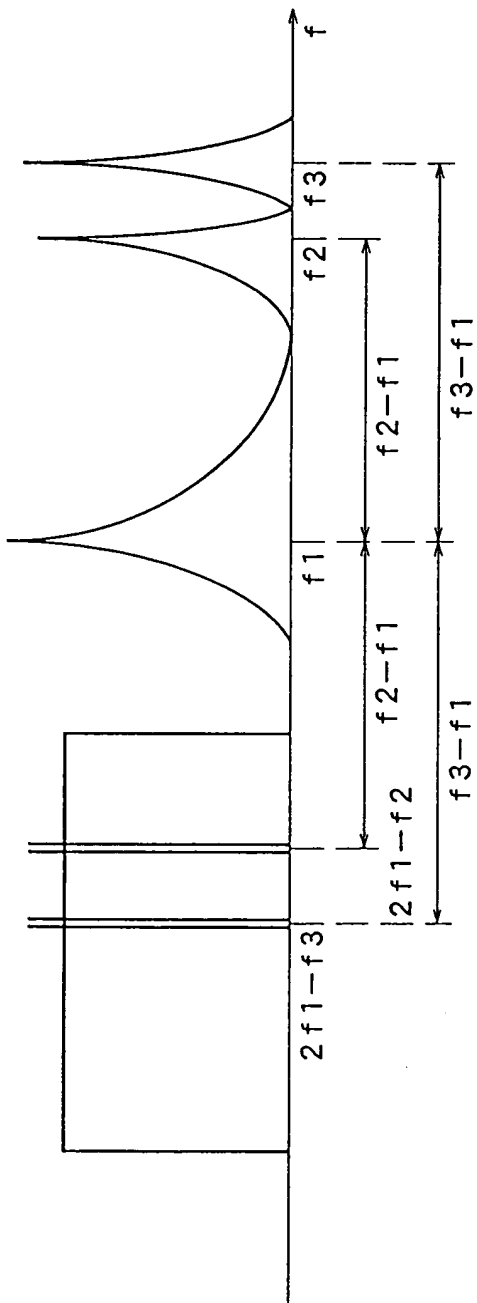


Fig. 9A

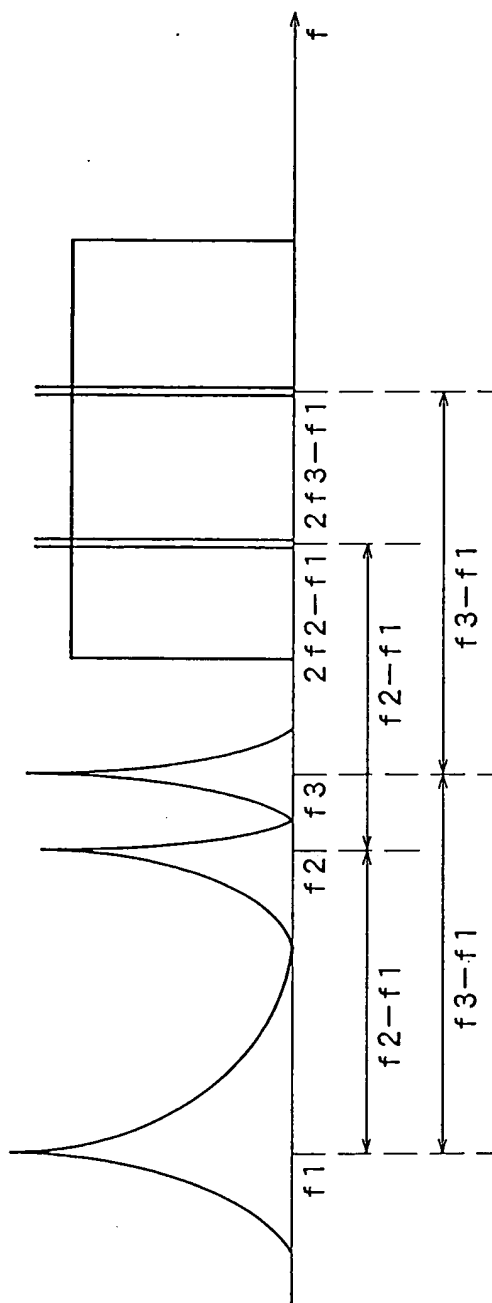


Fig. 9B

Fig. 10

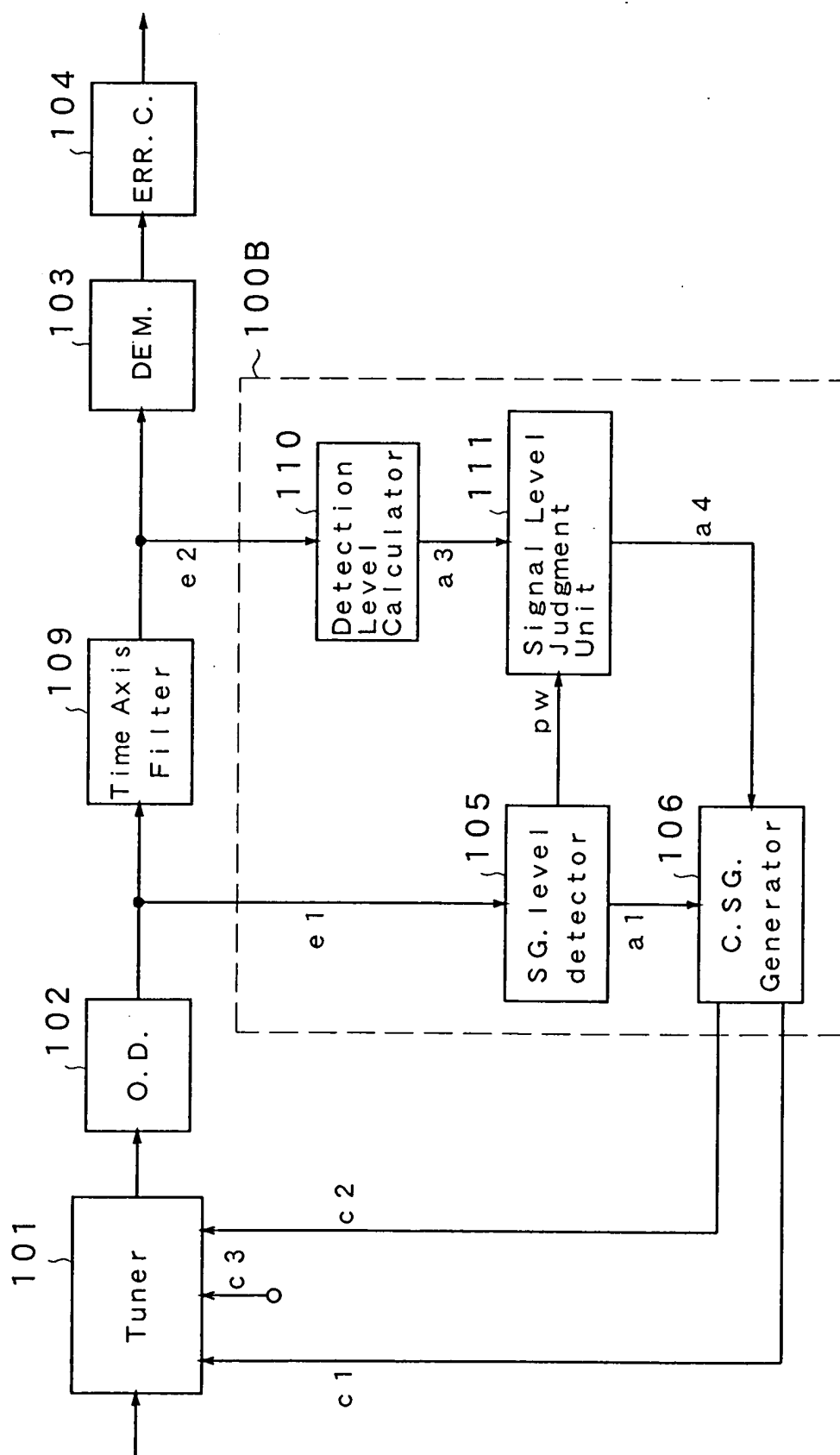


Fig. 11

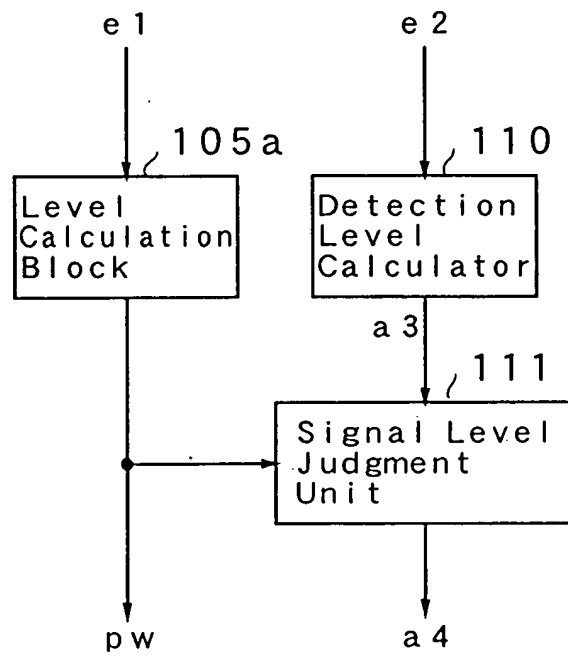
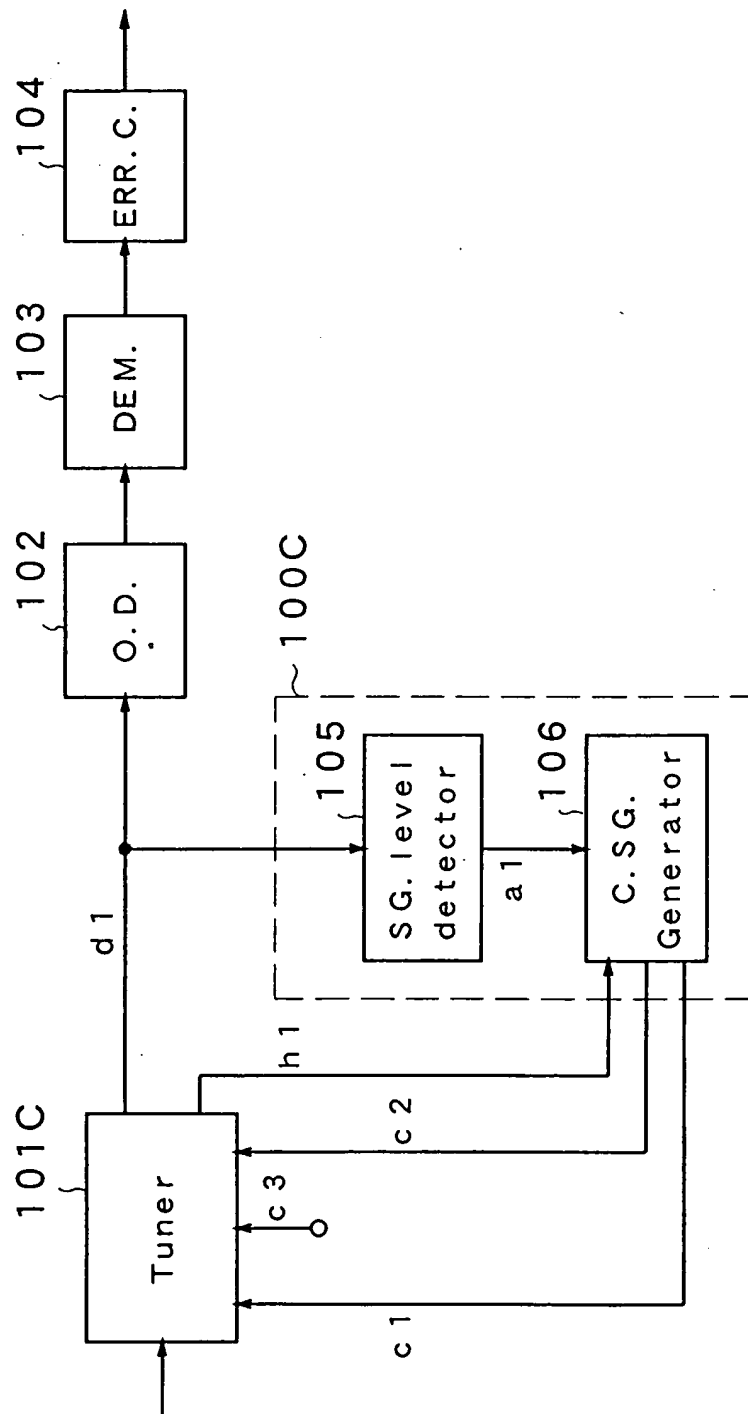


Fig. 12



F i g. 1 3

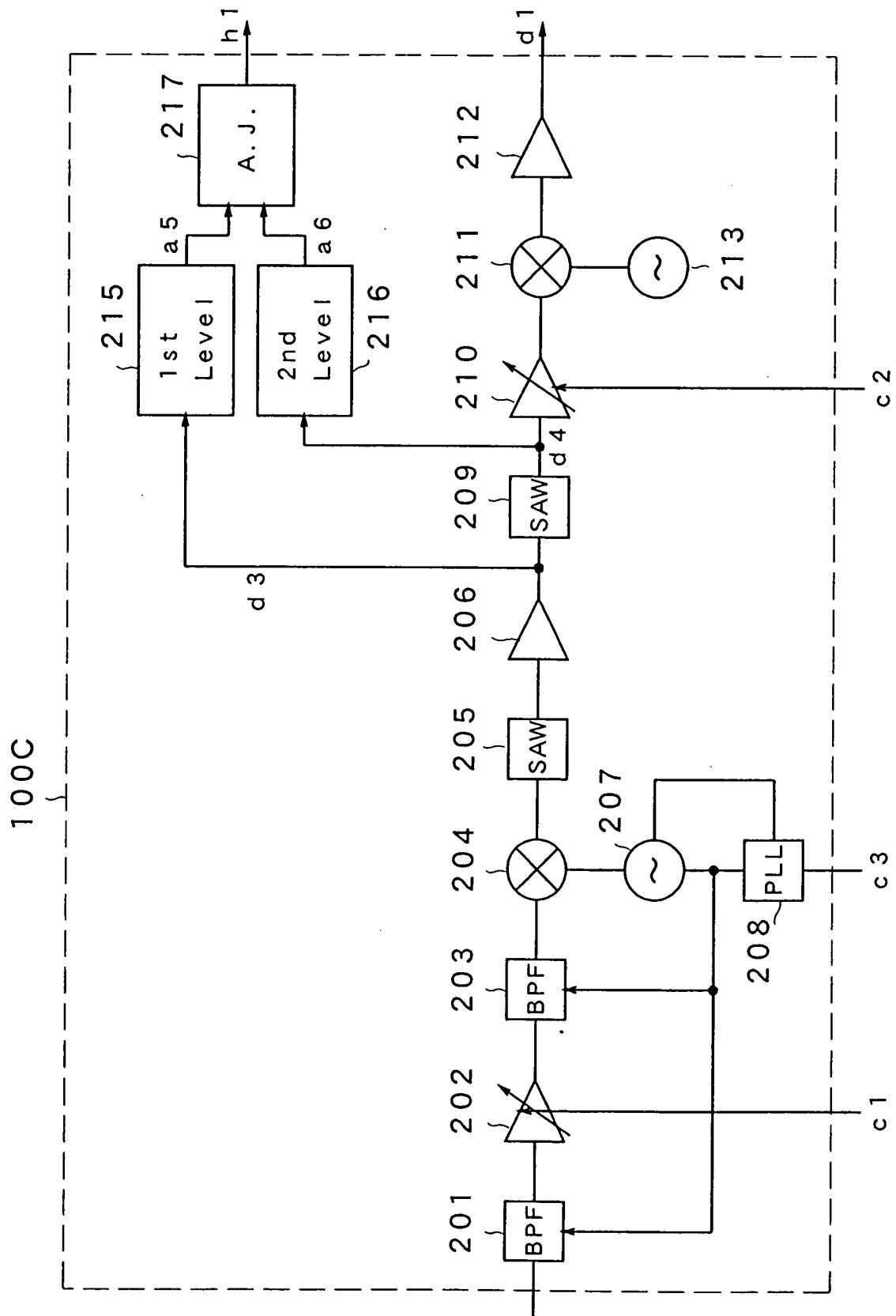


Fig. 14

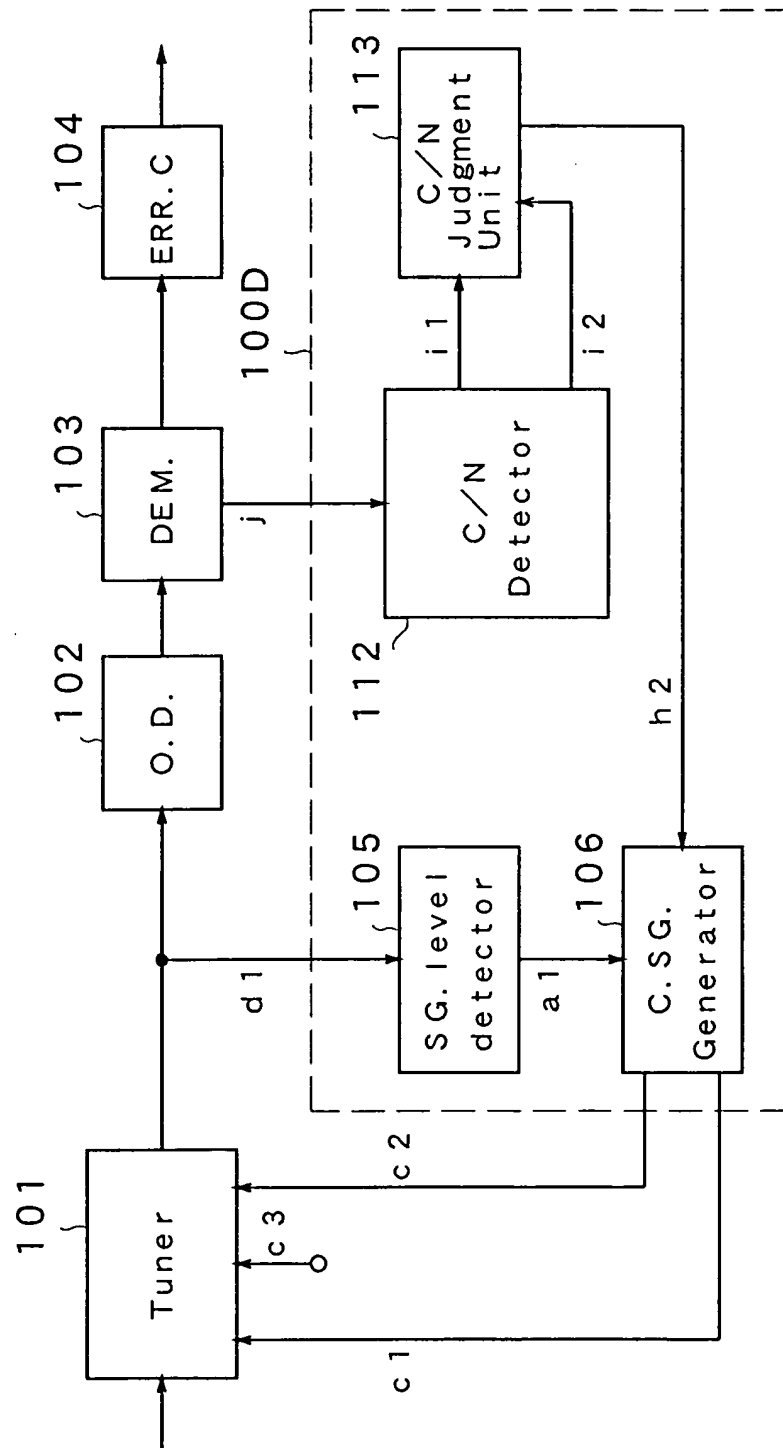


Fig. 15

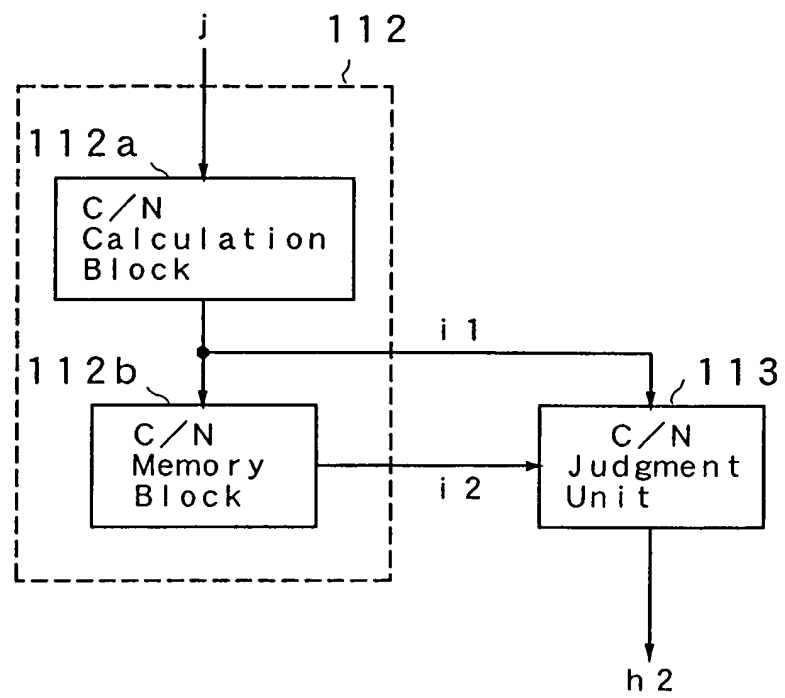


Fig. 16

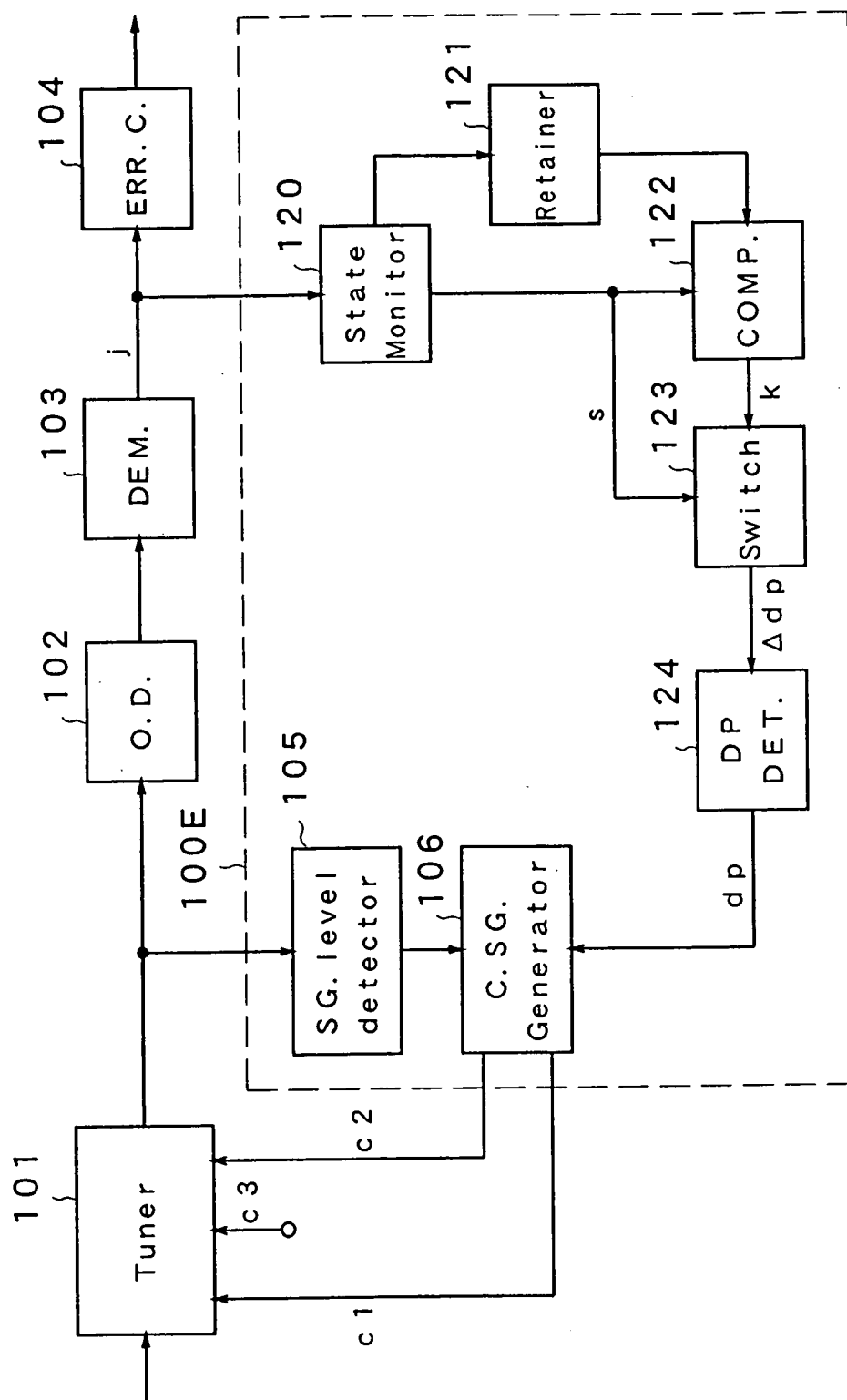


Fig. 17

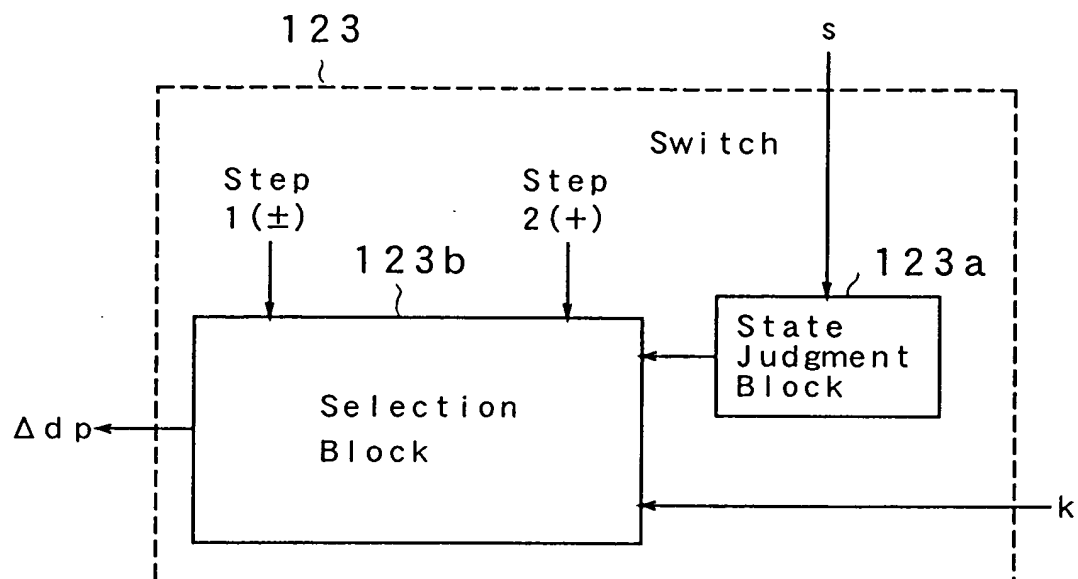


Fig. 18

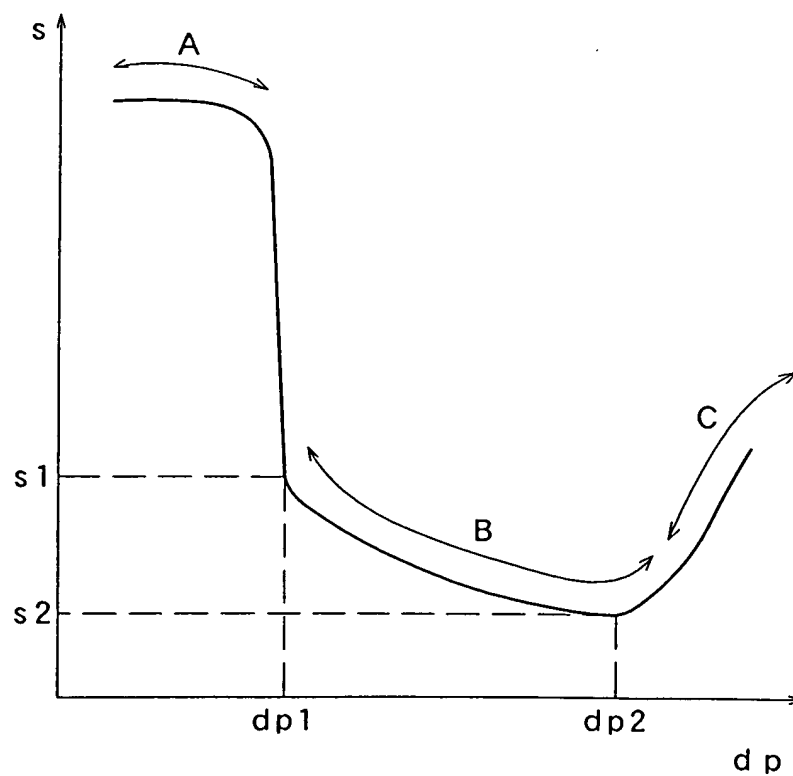


Fig. 20

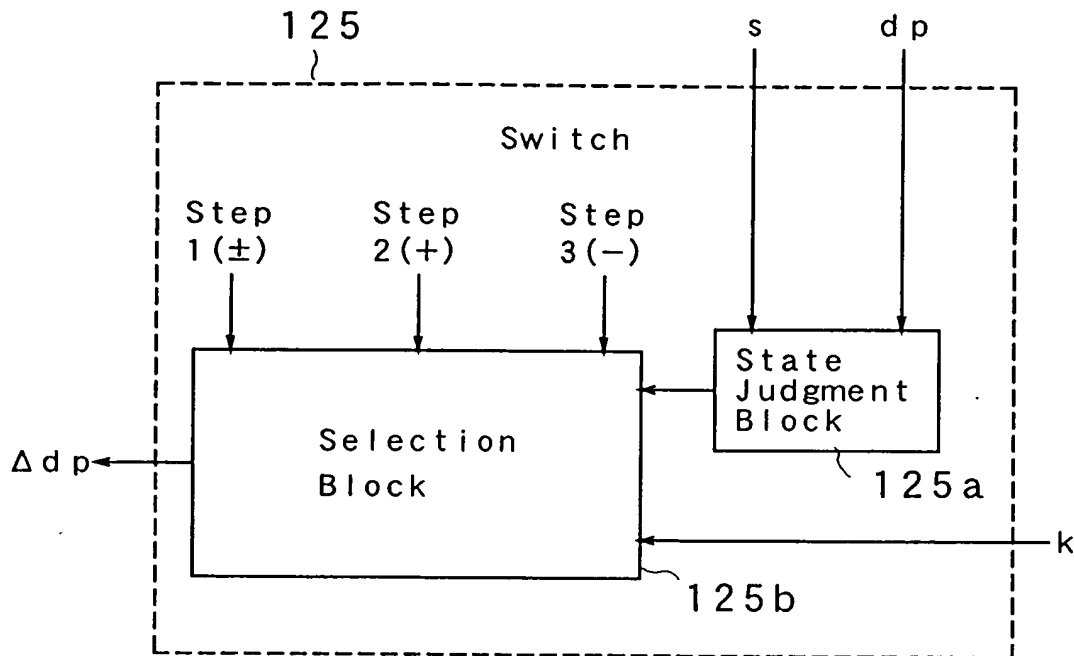


Fig. 21

Demodulation State	dp	Step Signal
○	large	step 1 (± : small)
×	large	step 3 (- : large)
○	small	step 1 (± : small)
×	small	step 2 (+ : large)

Fig. 23

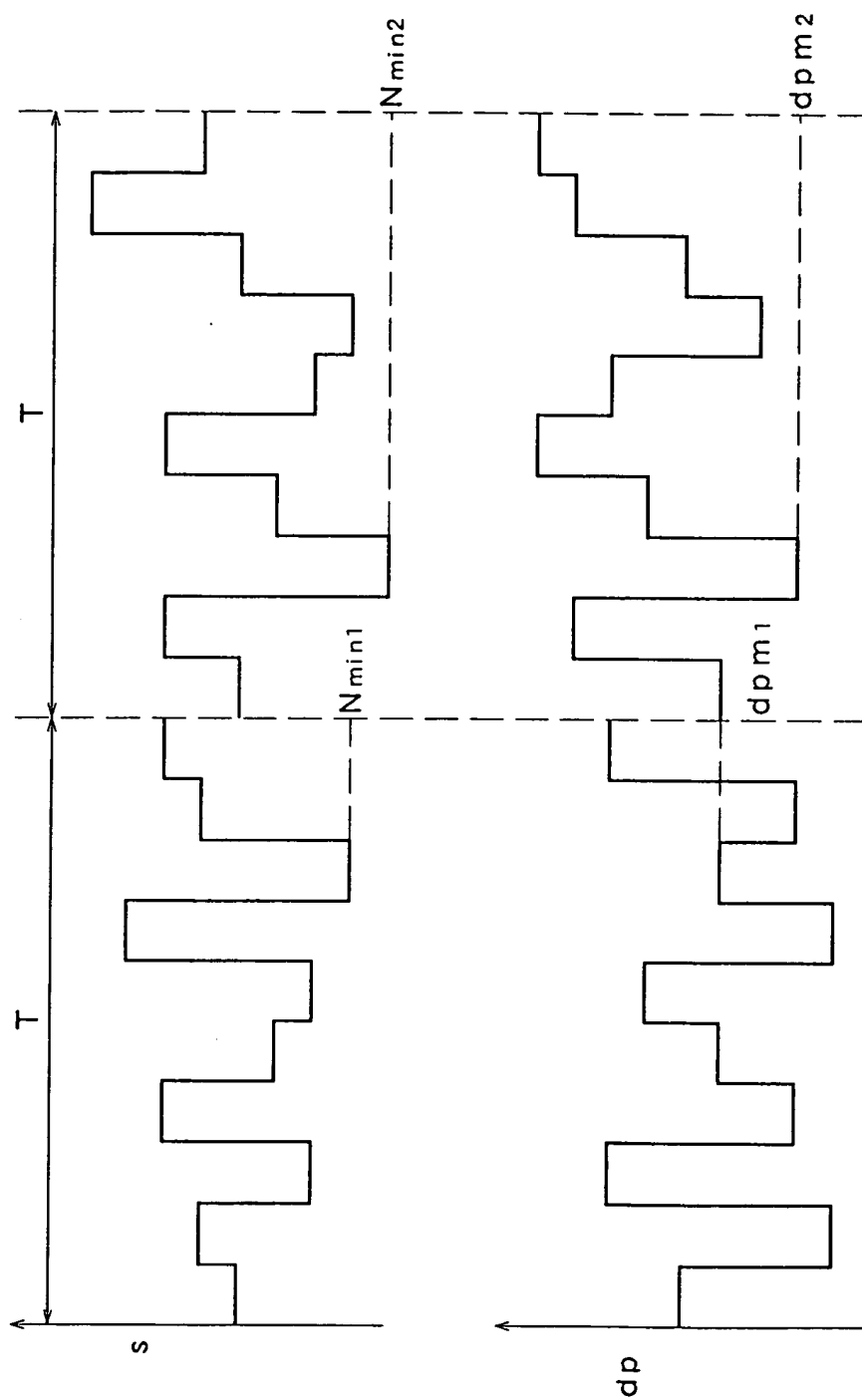


Fig. 25

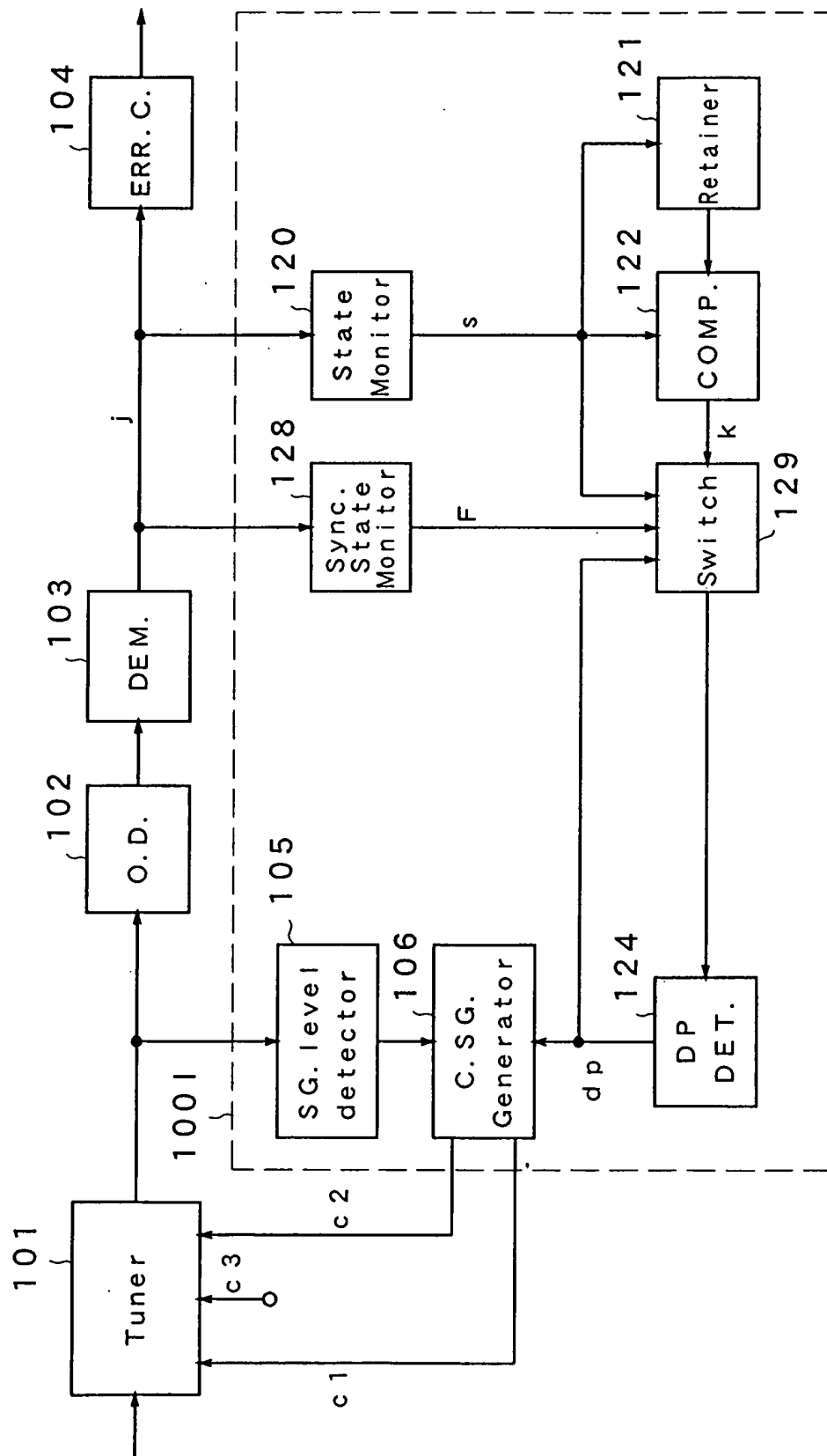


Fig. 26

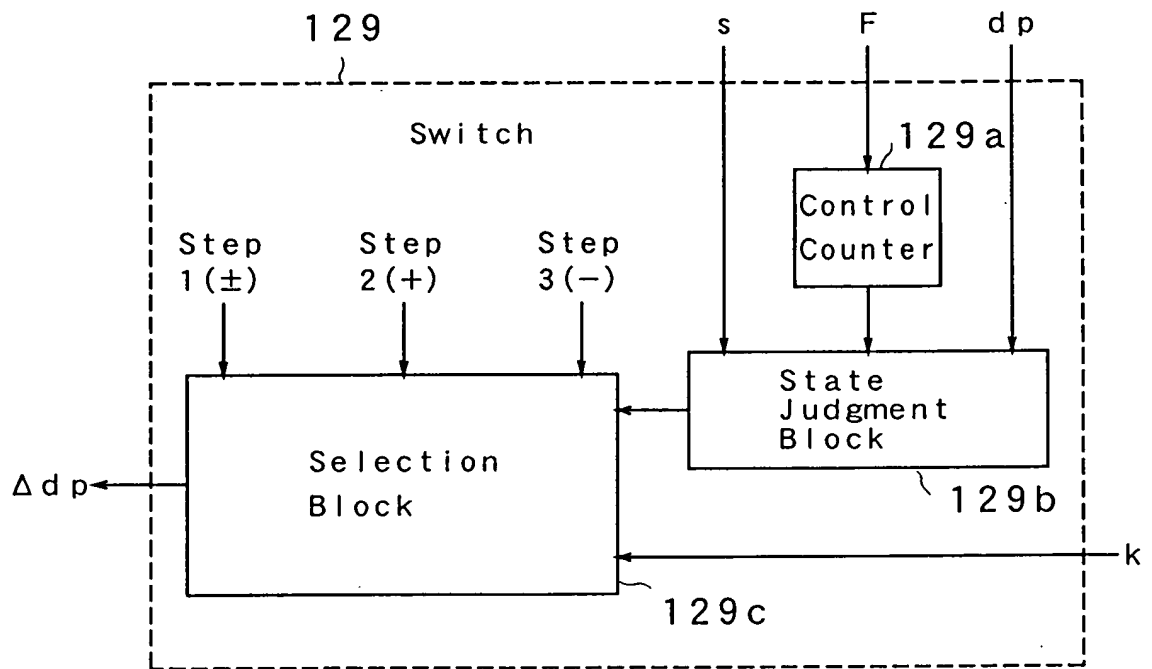


Fig. 27

N	Demodulation State	Comparison Signal	dp	Step Signal
0	○	—	—	step 1 (± : small)
0	×	—	—	step 2 (+ : large)
1	—	—	small	step 1 (± : small)
1	—	better	large	step 1 (± : small)
1	—	same or worse	large	step 3 (— : large)
2 or above	—	—	—	step 1 (± : small)

Fig. 28

